

珠海 Zhuhai 全志科技 Allwinner Technology Campus Recruitment [Sept 4, 2012]:

148 people <http://www.allwinnertech.com/awt/campusRecruitment.html>

<p>Algorithm design engineers (15):</p> <ul style="list-style-type: none"> - <u>Responsibilities</u>: audio and video codec, audio processing, image processing, intelligent recognition, network transmission algorithm development, porting applications, IC verification work. - <u>Job direction</u>: audio and video codec, audio processing, image processing, intelligent recognition, multimedia middleware, multimedia network protocols. - <u>Qualifications</u>: <ol style="list-style-type: none"> 1. master's, doctoral degrees, mathematics, physics, signal processing, communications, electronics, mode identification and other related professionals; 2. skilled use of C / C + +, Matlab and other programming tools; 3. with good software programming design habits; 4. has good math skills, master the basis of digital signal processing; 5. audio and video algorithm research and development experience preferred; 6. with a good team spirit and self-motivational skills; 7. have good English reading and writing ability. 	<p>Systems design engineers (20):</p> <ul style="list-style-type: none"> - <u>Responsible</u> for IC system design. - <u>Job direction</u>: specifications for the IC internal module and architecture, FPGA verification, validation of engineering samples, internal module of driver development, application schematic design and R & D work. - <u>Qualifications</u>: <ol style="list-style-type: none"> 1. undergraduate, master's and doctoral degrees, electronic class and other relevant professionals; 2. have the basic analog / digital circuit theory foundation; 3. proficient in C / C + + language; 4. with good teamwork spirit and communication, coordination capacity; 5. basic good English reading and writing skills.
<p>IC CAD Engineer (10):</p> <ul style="list-style-type: none"> - <u>Responsibilities</u>: automatic placement and routing, improve VLSI IC flow. - <u>Job direction</u>: IC CAD design. - <u>Qualifications</u>: <ol style="list-style-type: none"> 1. undergraduate, master's, and doctoral degrees, microelectronics or physical the professional; 2. has a solid basic knowledge of the hardware circuit; 3. skilled master of various EDA Tools; 4. master of Verilog, TCL, Perl, C language; 5. experience of backend APR. 	<p>IC layout engineers (10):</p> <ul style="list-style-type: none"> - <u>Responsibilities</u>: engaged in IC layout design. - <u>Job direction</u>: IC layout physical layout and implementation. - <u>Qualifications</u>: <ol style="list-style-type: none"> 1. undergraduate, master's degree, electronic technology and technology, microelectronics or physical related professionals; 2. familiar with semiconductor device physics, CMOS integrated circuit manufacturing process, the use of the integrated circuit design (digital, analog); 3. master of mainstream EDA tools; 4. strong sense of responsibility, practical work, rigorous and meticulous.
<p>IC digital design engineer (15):</p> <ul style="list-style-type: none"> - <u>Responsibilities</u>: engaged in the internal logic of ultra-large-scale digital integrated circuit design and verification, including the use of EDA tools. - <u>Job direction</u>: system design and FPGA verification of digital circuits, image processing and video to strengthen the protocol stack development, wireless communications baseband design. - <u>Qualifications</u>: <ol style="list-style-type: none"> 1. master's and doctoral degrees, microelectronics or electronic related professional; 2. master the Verilog and multiple EDA tool, familiar ASIC developers; 3. there is a complete digital circuit design project experience in the design and verification; 4. to know more about the Audio, Video , CPU, communication, signal processing and error correction code design; 5. have a good sense of continuous learning, communication, and teamwork ability. 	<p>IC Analog Design Engineer (10):</p> <ul style="list-style-type: none"> - <u>Responsibilities</u>: engaged in the development of specifications for analog circuits and systems, circuit design, layout planning and inspection, the actual circuit verification, the actual test method, check the application program. - <u>Job direction</u>: SERDES, ADC / DAC, PLL, PMU, RF circuits such as R & D and design. - <u>Qualifications</u>: <ol style="list-style-type: none"> 1. master's and doctoral degrees, microelectronics or physical related professionals; 2. familiar with semiconductor device physics, CMOS integrated circuit manufacturing processes, integrated circuit design (digital, analog); 3. master of EDA tools, Matlab or C, Verilog, VHDL language, the actual project experience and ability.
<p>Technical Support Engineer (20):</p> <ul style="list-style-type: none"> - <u>Responsibilities</u>: system design, PCB layout and Layout, simulation, and technical support. - <u>Job direction</u>: system research and design, solve customer technical applications problems. - <u>Qualifications</u>: <ol style="list-style-type: none"> 1. undergraduate, master's degree, electronic related professional; 2. familiar with a PCB draw EDA software; 3. familiar with the use of basic measuring instruments, such as oscilloscopes; 4. have basic analog / digital circuit theoretical basis; 5. participated in experience over a variety of electronic design contest or personally designed electronic products is preferred; 6. has good communication skills and teamwork. 	<p>Test Development Engineer (8):</p> <ul style="list-style-type: none"> - <u>Responsibilities</u>: engaged in the development of the of chip engineering test system and complete the task of chip testing. - <u>Job direction</u>: the SoC product testing. - <u>Qualifications</u>: <ol style="list-style-type: none"> 1. undergraduate, master's degree, electronics, automation, measurement instruments related professionals; 2. familiar with analog knowledge of digital circuits, circuit development experience; 3. familiar with programming languages such as VC / VB; 4. skilled use Protel / ORCAD PCB schematic design; 5. a strong sense of responsibility, practical work, rigorous and meticulous.
<p>Software Design Engineer (40):</p> <ul style="list-style-type: none"> - <u>Responsible</u> for software design. - <u>Job direction</u>: <ol style="list-style-type: none"> 1. Linux system development (drivers, transplation, optimization tools, the bootloader, multi-core / OpenCL); 2. Android system development (network communication, multimedia, graphics / OpenGL, identification, security); 3. Android, IOS application development (Java framework development, application development, network client, games); 4. web development (front-end design, web services, html5, database, jQuery, Javascript); 5. an automated test system development - <u>Qualifications</u>: <ol style="list-style-type: none"> 1. undergraduate, master's and Ph.D. degree, computer software, architecture, communication / information engineering / automation / measurement instruments, application of mathematical and other professionals; 2. proficient in C / C + + programming; 3. the software foundation of knowledge solid, clear programming related to the concept of ; 4. clear thinking, logic programming style specifications, inquisitive research. 	